

	Type	Hits	Search Text	DBs	Time Stamp
1	BRS	905	network near chip	US-PGPUB; USPAT; EPO; JPO	2004/05/05 18:48
2	BRS	182	external near logic near (unit device circuit module node adapter card system loadboard)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 18:54
3	BRS	85397	(exten\$4 expan\$4) near (port unit device node card module card)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 18:53
4	BRS	3846	((exten\$4 expan\$4) near (port unit device node card module card)) and packet	US-PGPUB; USPAT; EPO; JPO	2004/05/05 18:53
5	BRS	66	((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip))	US-PGPUB; USPAT; EPO; JPO	2004/05/05 18:53
6	BRS	9533	external with logic with (unit device circuit module node adapter card system loadboard)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:01
7	BRS	20	(((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard)))	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:02
8	BRS	9	(((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard))) and @ad<"20010125"	US-PGPUB; USPAT; EPO; JPO	2004/05/05 18:55
9	BRS	4	(((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard))) and @ad<"20010125") and test\$3	US-PGPUB; USPAT; EPO; JPO	2004/05/05 18:55
10	BRS	377458	external with (unit device circuit module node adapter card system loadboard)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:04
11	BRS	9533	(external with (unit device circuit module node adapter card system loadboard)) and (external with logic with (unit device circuit module node adapter card system loadboard))	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:03

	Type	Hits	Search Text	DBs	Time Stamp
12	BRS	9533	(external with logic with (unit device circuit module node adapter card system loadboard)) and (external with (unit device circuit module node adapter card system loadboard))	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:03
13	BRS	87895	external near (unit device circuit module node adapter card system loadboard)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:04
14	BRS	26066	(external near (unit device circuit module node adapter card system loadboard)) and test\$3	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:04
15	BRS	2	(((((exten\$4 expan\$4) near (port unit device node card module card)) and packet) and (network near chip)) and (external with logic with (unit device circuit module node adapter card system loadboard))) and ((external near (unit device circuit module node adapter card system loadboard)) and test\$3)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:05
16	BRS	1	(network near chip) and (external near logic near (unit device circuit module node adapter card system loadboard))	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:11
17	IS&R	1	("5953335").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/05 19:32
18	IS&R	1	("5193110").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/05 19:32
19	IS&R	1	("5231633").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/05 19:33
20	BRS	108994	test\$3 and (expansion extending near (port unit device node circuit device module))	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:36
21	BRS	28924	(test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:36
22	BRS	3468	((test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)) and (external near (device unit node system circuit board loadboard adapter module FPGA))	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:38

	Type	Hits	Search Text	DBs	Time Stamp
23	BRS	53	(((((test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)) and (external near (device unit node system circuit board loadboard adapter module FPGA))) and (network near chip)	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:38
24	BRS	33	(((((test\$3 and (expansion extending near (port unit device node circuit device module))) and (packet frame)) and (external near (device unit node system circuit board loadboard adapter module FPGA))) and (network near chip)) and @ad<"20010125"	US-PGPUB; USPAT; EPO; JPO	2004/05/05 19:39
25	BRS	69609	"370"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:27
26	BRS	10952	"370"/\$.ccls. and test\$3	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:28
27	BRS	8809	("370"/\$.ccls. and test\$3) and @ad<"20010125"	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:47
28	BRS	480	((("370"/\$.ccls. and test\$3) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard)))	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:49
29	BRS	300	((("370"/\$.ccls. and test\$3) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:49
30	BRS	12	(((((("370"/\$.ccls. and test\$3) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (network with chip)	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:50
31	BRS	29109	(test\$4 same (chip (network near switch\$4))) and @ad<"20010125"	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:48

	Type	Hits	Search Text	DBs	Time Stamp
32	BRS	1227	((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:49
33	BRS	741	(((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:50
34	BRS	75	((((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (network with chip)	US-PGPUB; USPAT; EPO; JPO	2004/05/06 09:51
35	BRS	321	((((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))	US-PGPUB; USPAT; EPO; JPO	2004/05/06 10:57
36	BRS	182	((((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))	US-PGPUB; USPAT; EPO; JPO	2004/05/06 10:59

	Type	Hits	Search Text	DBs	Time Stamp
37	BRS	80	(((((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array)))	US-PGPUB; USPAT; EPO; JPO	2004/05/06 11:00
38	BRS	51	(((((((((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array))) and (measur\$3 detect\$3 verif\$3 evaluat\$3)) and (DA (destination near address)) and (SA (source near address)))	US-PGPUB; USPAT; EPO; JPO	2004/05/06 11:03
39	BRS	50	(((((((((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array))) and (measur\$3 detect\$3 verif\$3 evaluat\$3)) and (DA (destination near address)) and (SA (source near address))) and (swap\$4 exchang\$3)	US-PGPUB; USPAT; EPO; JPO	2004/05/06 11:04

	Type	Hits	Search Text	DBs	Time Stamp
40	BRS	80	(((((test\$4 same (chip (network near switch\$4))) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))) and (logic near (unit device node loadboard interface circuit board adapter module))) and (FPGA (gate near array))) and (measur\$3 detect\$3 verif\$3 evaluat\$3)	US-PGPUB; USPAT; EPO; JPO	2004/05/06 11:28
41	BRS	117	((("370"/\$.ccls. and test\$3) and @ad<"20010125") and ((expan\$4 extend\$4) near (port device unit circuit unit node module board adapter interface loadboard))) and (frame packet data cell) and external) and (external near (unit device node module circuit \$4board system))	US-PGPUB; USPAT; EPO; JPO	2004/05/06 12:19
42	IS&R	1	("5588152").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/06 20:27
43	IS&R	1	("6028845").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/11 09:35
44	IS&R	1	("6463037").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/11 09:18
45	IS&R	1	("5477531").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/11 09:18
46	IS&R	1	("6023455").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/11 09:19
47	IS&R	1	("6404740").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/11 09:19
48	IS&R	1	("5383177").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO	2004/05/11 09:35
49	BRS	5452	370/241-253.ccls.	US-PGPUB; USPAT; EPO; JPO	2004/11/16 10:03

	Type	Hits	Search Text	DBs	Time Stamp
50	BRS	4443	S50 and (@ad<"20010125" @rlad<"20010125" @rlan<"20010125")	US-PGPUB; USPAT; EPO; JPO	2004/11/16 12:05
51	BRS	181	S51 and (port with frame)	US-PGPUB; USPAT; EPO; JPO	2004/11/16 10:06
52	BRS	13	S52 and (expansion with port)	US-PGPUB; USPAT; EPO; JPO	2004/11/16 12:06
53	BRS	3	S53 and test\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 10:07
54	BRS	86	S52 and test\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 10:20
55	BRS	22	S54 and (external near (unit tester adapter device fpga))	US-PGPUB; USPAT; EPO; JPO	2004/11/16 12:04
56	BRS	1	"5953335".pn.	US-PGPUB; USPAT; EPO; JPO	2004/11/16 11:09
57	BRS	144	(external near logic near (unit tester adapter device fpga))	US-PGPUB; USPAT; EPO; JPO	2004/11/16 12:05
58	BRS	117	S58 and (@ad<"20010125" @rlad<"20010125" @rlan<"20010125")	US-PGPUB; USPAT; EPO; JPO	2004/11/17 10:03
59	BRS	1	S59 and (expansion with port)	US-PGPUB; USPAT; EPO; JPO	2004/11/16 12:07
60	BRS	1932	expansion near port	US-PGPUB; USPAT; EPO; JPO	2004/11/16 12:07
61	BRS	1457	S61 and (@ad<"20010125" @rlad<"20010125" @rlan<"20010125")	US-PGPUB; USPAT; EPO; JPO	2005/02/11 16:19
62	BRS	12	S62 and (external near4 (device logic test\$3 unit)) and (switch\$3 near2 chip) and test\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 12:09
63	BRS	1	"5586111".pn.	US-PGPUB; USPAT; EPO; JPO	2004/11/16 14:30
64	BRS	1	"4970466".pn.	US-PGPUB; USPAT; EPO; JPO	2004/11/16 15:01

	Type	Hits	Search Text	DBs	Time Stamp
65	BRS	362	uut and (tester (test\$3 near (logic device adapter interface unit)))	US-PGPUB; USPAT; EPO; JPO	2004/11/16 15:03
66	BRS	4	S68 and frame	US-PGPUB; USPAT; EPO; JPO	2004/11/16 15:04
67	BRS	5	S68 and (frame packet)	US-PGPUB; USPAT; EPO; JPO	2004/11/16 15:04
68	BRS	20	S67 and (switch\$3 with (chip ic (integrated near circuit)))	US-PGPUB; USPAT; EPO; JPO	2004/11/16 15:13
69	BRS	221	steven near2 nguyen.xp.	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:02
70	BRS	5	steven near2 nguyen.xp. and network near2 analyz\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:03
71	BRS	16	steven near2 nguyen.xp. and network with analyz\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:04
72	BRS	172	"324"/\$.ccls. and UUT	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:06
73	BRS	91	"324"/\$.ccls. and UUT and switch	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:06
74	BRS	3	"324"/\$.ccls. and UUT and switch with chip	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:07
75	BRS	4	"324"/\$.ccls. and UUT and switch\$3 with chip	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:08
76	BRS	29	"324"/\$.ccls. and UUT and network	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:08

77	BRS	35	("3434049" "4580872" "4766386" "4890278" "5048009" "5115449" "5185735" "5198805" "5226036" "5231593" "5243543" "5293635" "5309428" "5329519" "5341400" "5365509" "5367670" "5377196" "5381348" "5382910" "5383178" "5387902" "5425017" "5436555" "5461318" "5463735" "5477531" "5532603" "5539659" "5577023" "5586054" "5619532" "5664105" "5703883").PN.	US-PGPUB; USPAT; USOCR	2004/11/16 17:11
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	Type	Hits	Search Text	DBs	Time Stamp
78	BRS	2	(switch\$3 near network\$3 near chip) with test\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:42
79	BRS	3	(switch\$3 near2 network\$3 near2 chip) with test\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:44
80	BRS	8	(switch\$3 near4 network\$3 near4 chip) with test\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/16 17:44
81	BRS	139	"714"/\$.ccls. and uut	US-PGPUB; USPAT; EPO; JPO	2004/11/17 10:03
82	BRS	122	S83 and (@ad<"20010125" @rlad<"20010125" @rlan<"20010125")	US-PGPUB; USPAT; EPO; JPO	2004/11/17 10:03
83	BRS	0	S84 and (network near2 switch\$3 near2 chip)	US-PGPUB; USPAT; EPO; JPO	2004/11/17 10:04
84	BRS	8	S84 and (network near2 switch\$3)	US-PGPUB; USPAT; EPO; JPO	2004/11/17 10:04
85	BRS	8	S86 and test\$3	US-PGPUB; USPAT; EPO; JPO	2004/11/17 10:06
86	BRS	4789	370/242-253.ccls.	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:28
87	BRS	3748	S94 and (@ad<"20010125" @rlad<"20010125" @rlan<"20010125")	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:34
88	BRS	46	S95 and test\$3 and (network with chip)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:34
89	BRS	0	S96 and ((expansion exten\$5) with frame with port)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:32
90	BRS	0	S96 and (((expansion exten\$5) with frame) with port)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:31
91	BRS	0	S96 and (((expansion exten\$5) same frame) same port)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:33
92	BRS	7	S96 and (frame with port)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:32

	Type	Hits	Search Text	DBs	Time Stamp
93	BRS	1	S96 and (((expansion external exten\$5) same frame) same port)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:36
94	BRS	1151000 7	((@ad<"20010125" @rlad<"20010125" @rlan<"20010125")	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:34
95	BRS	3030	S102 and test\$3 and (network with chip)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:35
96	BRS	435	S102 and (test\$3 same (network with chip))	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:35
97	BRS	7	S104 and (((expansion external exten\$5) same frame) same port)	US-PGPUB; USPAT; EPO; JPO	2005/02/14 10:36